

BranchSpec: Information Leakage Attacks Exploiting Speculative Branch Instruction Executions

Md Hafizul Islam Chowdhuryy

University of Central Florida
Orlando, FL

Hang Liu

Stevens Institute of Technology
Hoboken, NJ

Fan Yao

University of Central Florida
Orlando, FL

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Background

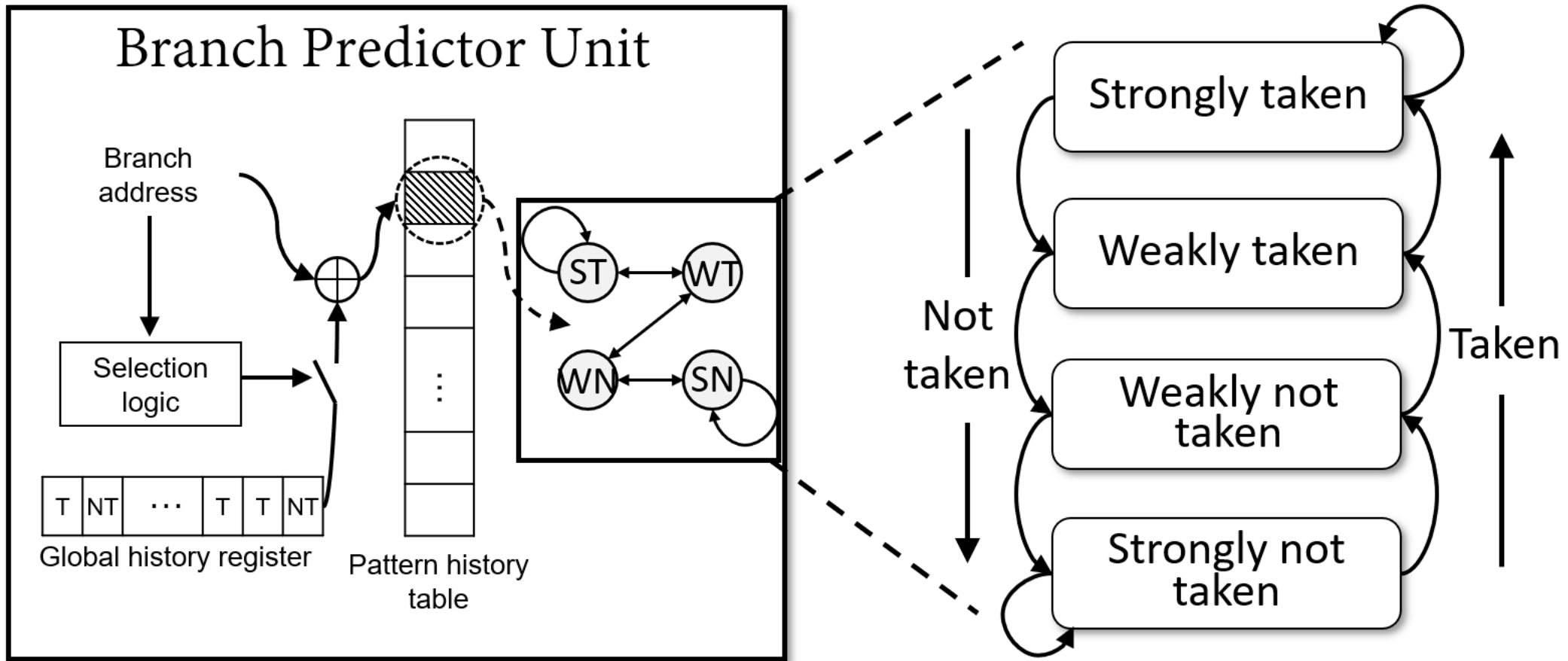
- ❖ Security issues of speculation are raising critical concerns.
- ❖ Microarchitectural state changes remain beyond speculation.
- ❖ Unintended data could be exfiltrated via side channels.
 - E.g., Spectre and Meltdown.
 - Demonstrated using Cache, TLB and function units.

Motivation

- ❖ Branch predictor unit (BPU) is one of the most critical components
- ❖ BPU is used to trigger mis-speculation in transient execution attacks
- ❖ BPU can transfer secret in non-speculative domain (e.g., BranchScope¹)

❖ Can we use branch predictor as **transmitting medium** in transient execution domain?

Modern Branch Predictor Architecture



Do PHT Changes Remain After Speculation?

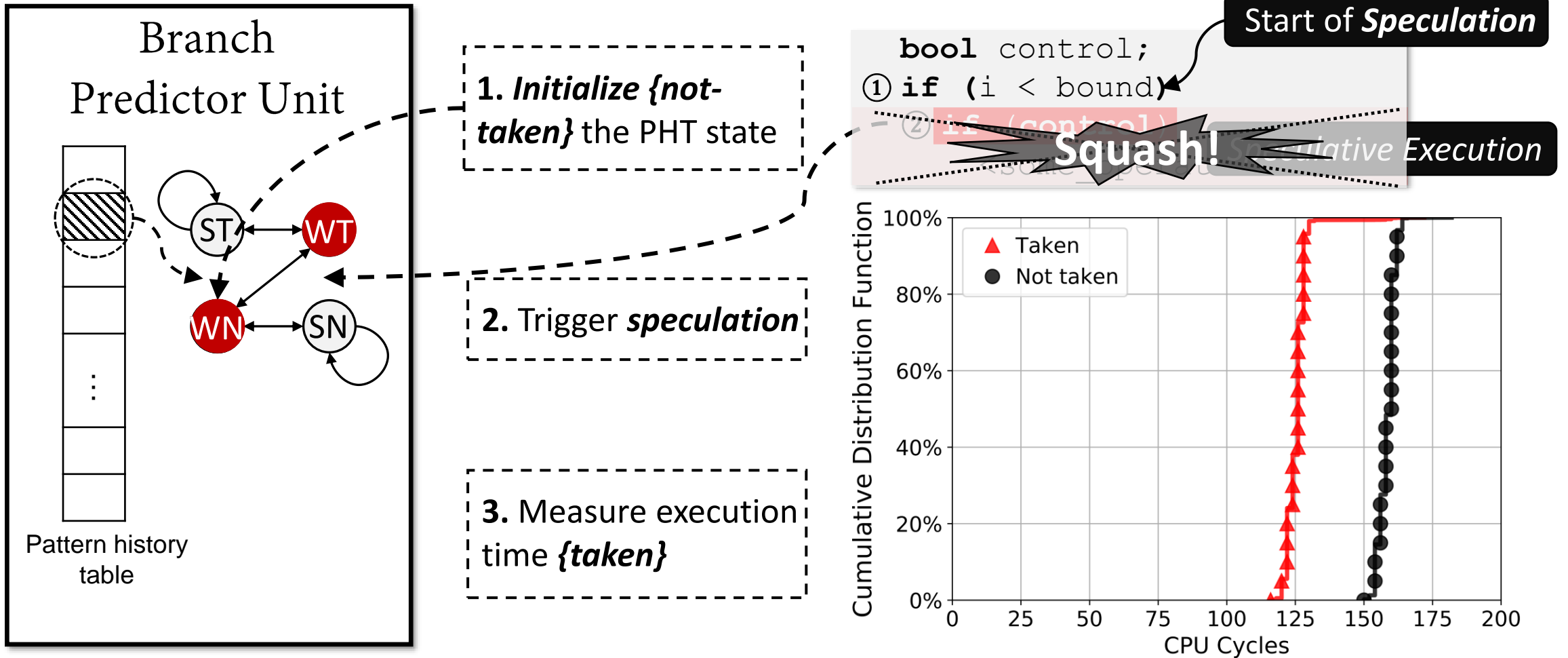
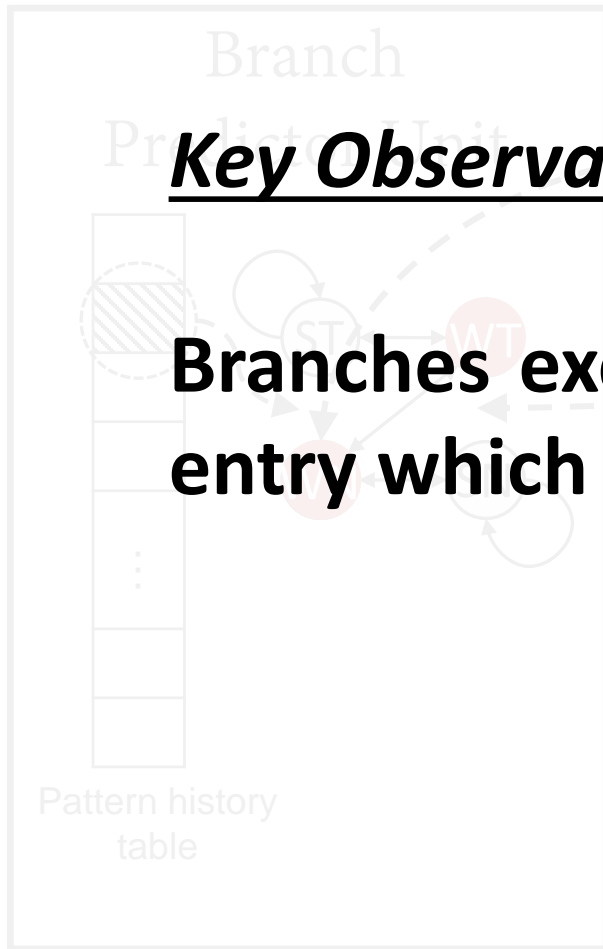


Figure 1: Execution time of branch ② in step 3 for different outcome of the branch in step 2.

Do PHT Changes Remain After Speculation?



Key Observation:

Branches executed in the speculative path change PHT entry which are not restored in case of mis-speculation.

1. Initialize {not-taken} the PHT state

3. Measure execution time {taken}

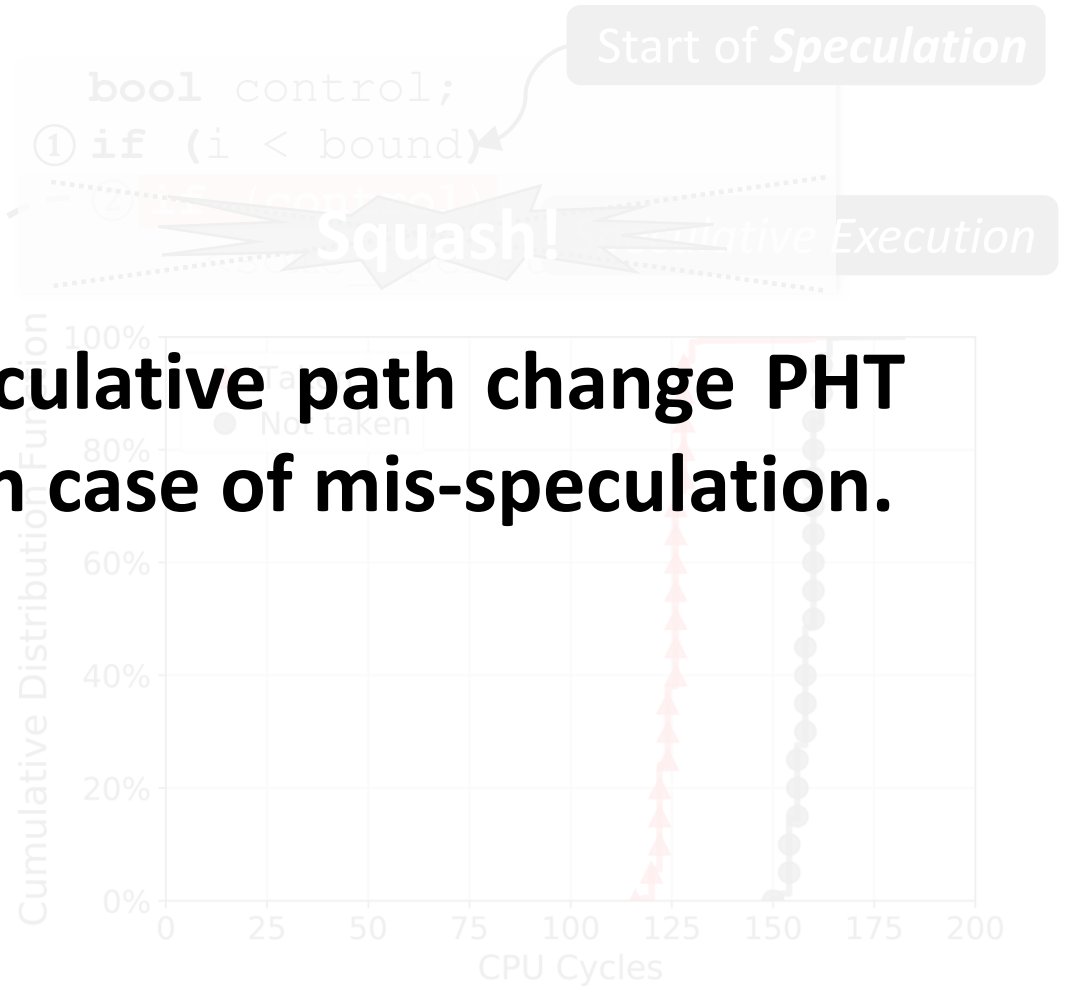


Figure 1: Execution time of branch (2) in step 3 for different outcome of the branch in step 2

BranchSpec: Side Channel Attack

Step 1: Preset PHT entry (PHT_v) of victim branch (b_v)

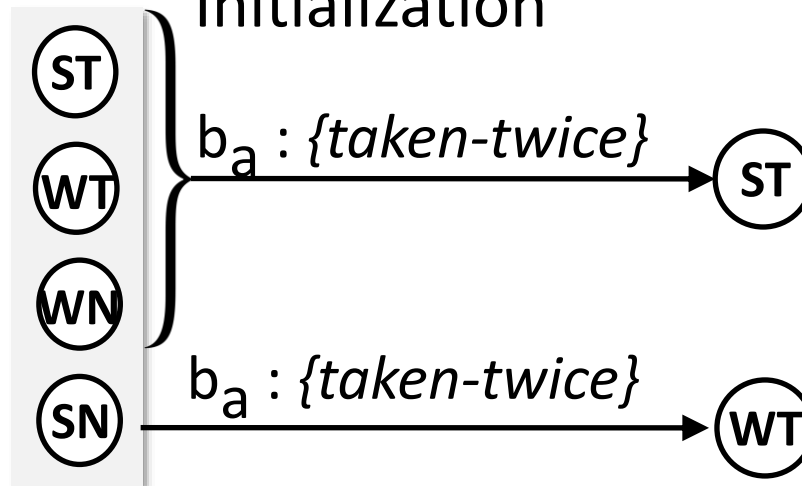
- Attacker uses a **congruent branch** of b_v (i.e., b_a)
- Executes b_a twice with *taken* outcome

Victim

```
// Parent branch
if (x < bound)
  ....
  ....
// Victim branch,  $b_v$ 
if (array1[x])
  <some_operations>;
```

Attacker:

Initialization



Initial state (PHT_v)

BranchSpec: Side Channel Attack

Victim

```
// Parent branch
if (x < bound)
    ....
    ....
// Victim branch,  $b_v$ 
if (array1[x])
    <some_operations>;
```

Step 2: Victim executes b_v speculatively

- Attacker can trigger mis-speculation of parent branch using congruent branch
- PHT entry of victim branch (PHT_v) is updated based on b_v outcome



BranchSpec: Side Channel Attack

Victim

// Parent branch, b_{v0}



Step 3: Attacker

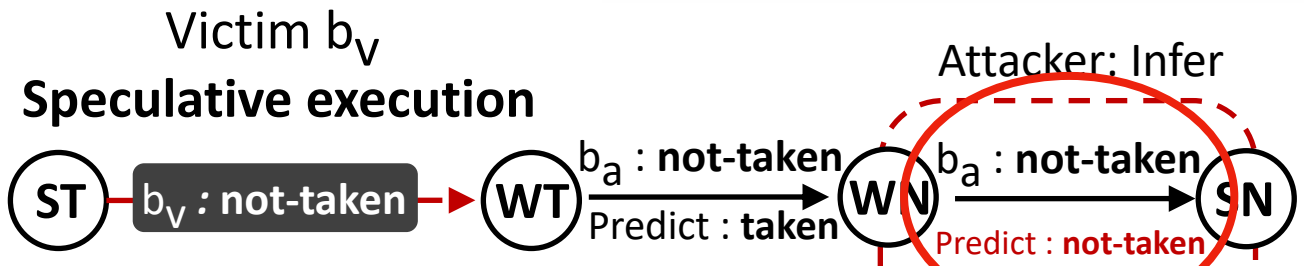
- Execute b_a twice with *not taken* outcome
- Measure execution time

Correct prediction of $b_a \rightarrow$ Shorter execution time

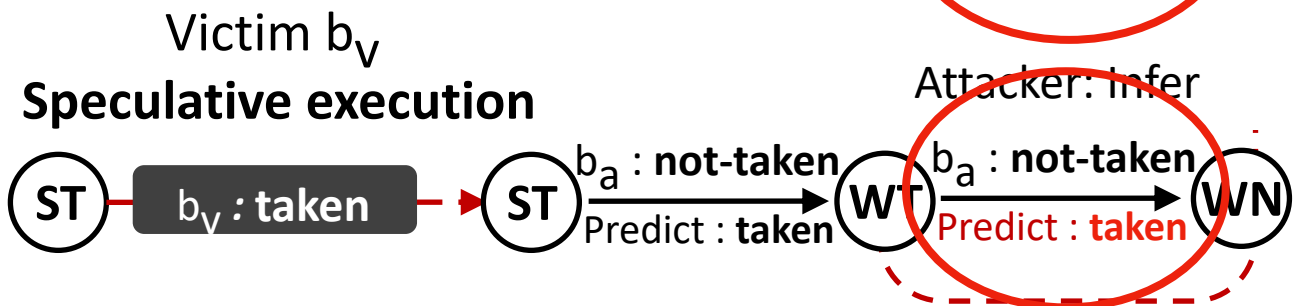
Mis-prediction of $b_a \rightarrow$ Longer execution time

<some_operations>;

b_v resolved as *Not taken*



b_v resolved as *taken*



Results and Characteristics of BranchSpec

- ❖ First work to show information leakage via branch predictor in transient execution attacks
 - Implemented on processors **with and w/o SMT**
 - Bit error rate is less than **4%**
 - Potentially targeted applications: **Crypto algorithms, image processing and ML programs**
- ❖ Enables even stronger attack capabilities
 - Completely uses BPU for end-to-end attack
 - Utilizes more common code patterns than Spectre V1

Spectre V1 Gadgets

```
if (x < array1_size)
    y = array2(array1[x] * 4096);
```

BranchSpec Gadgets

```
if (x < bound)
    if (array1[x]) //  $b_V$ 
        <some_operations>;
```

```
if (x < bound)
    for (i = 0; i < bound; i++)
        if (array1[x + i]) //  $b_V$ 
            <some_operations>;
```

```
for (i = x; i < bound; i++)
    if (array1[i]) //  $b_V$ 
        <some_operations>;
```

BranchSpec: Covert Channel Attack

❖ Covert channel using BranchSpec

- With optimizations, 131 Kbps transmission rate within 3.7% error rate

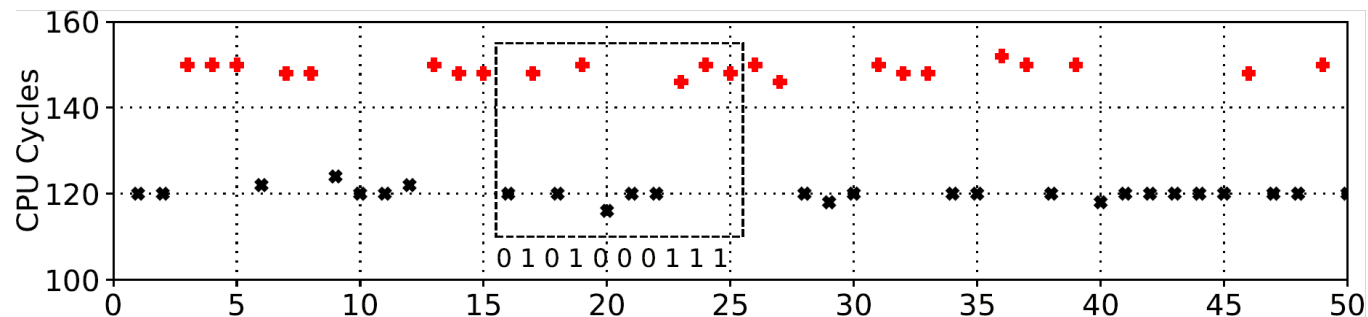


Figure 3: Latency traces for a 50-bit transmission by Spy corresponding to the covert channel in Figure 2.

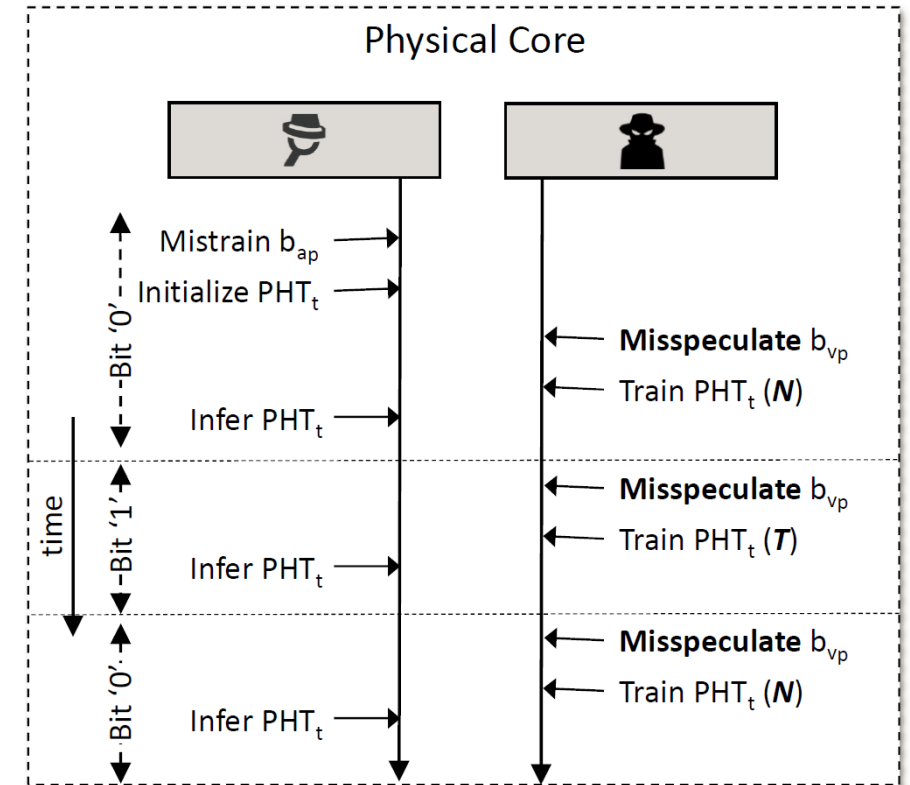


Figure 2: Illustration of BranchSpec covert channel protocol.

Potential Mitigations

- ❖ Existing system level defenses are ineffective
 - E.g., Retpoline, IBRS and others
- ❖ Potential architecture level mitigations
 - Restoring states for transient branches
 - Delaying PHT update
 - Enabling invisible PHT entry update

Conclusion

- ❖ Branches executed in speculation change PHT states, which are not restored after transient execution finishes.
- ❖ The vulnerability allows BPU to be used as *transmitting medium* in transient execution attacks.
- ❖ We demonstrate new forms of side and covert channels exploiting the discovered threat.
- ❖ We discuss potential mitigations to secure branch executions in speculative domain.

Thanks! Questions?

Md Hafizul Islam Chowdhuryy

Email: reyad@knights.ucf.edu

Source code available: <https://github.com/fanyao/branchspec>